

Attorney's Docket No.: 10559-576001 / P12790

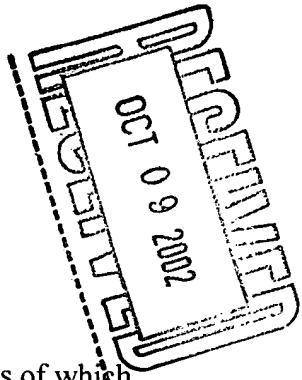
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Steve H. Weissinger
Serial No. : 10/076,975
Date : February 15, 2002
Title : OBTAINING CYCLIC REDUNDANCY CODES

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
Washington, D.C. 20231



INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 9/25/02

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

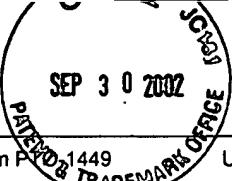
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Sheet 1 of 1

Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-576001	Application No. 10/076,975
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Steve H. Weissinger		
		Filing Date February 15, 2002	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						OCT 02 2002
	AH						
	AI						Technology Center 2100
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	AK						

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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes No
	AL						
	AM						
	AN						
	AO						
	AP						

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	Glaise et al., <i>Fast CRC Calculation</i> , 1993, IEEE
	AR	Feldmeier, <i>Fast Software Implementation of Error Detection Codes</i> , 12/1995, IEEE/ACM Transactions on Networking, Vol. 3, No. 6
	AS	Glaise, <i>A two-step computation of cyclic redundancy code CRC-32 for ATM networks</i> , 11/1997, IBM J. Res. Develop., Vol. 41 No. 6
	AT	Roginsky et al., <i>Efficient computation of packet CRC from partial CRCs with application to the Cells-In-Frames protocol</i> , 1998, Computer Communications 21, pp. 654-661
	AU	Joshi et al., <i>A New Parallel Algorithm for CRC Generation</i> , 2000, IEEE, pp. 1764-1768

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	